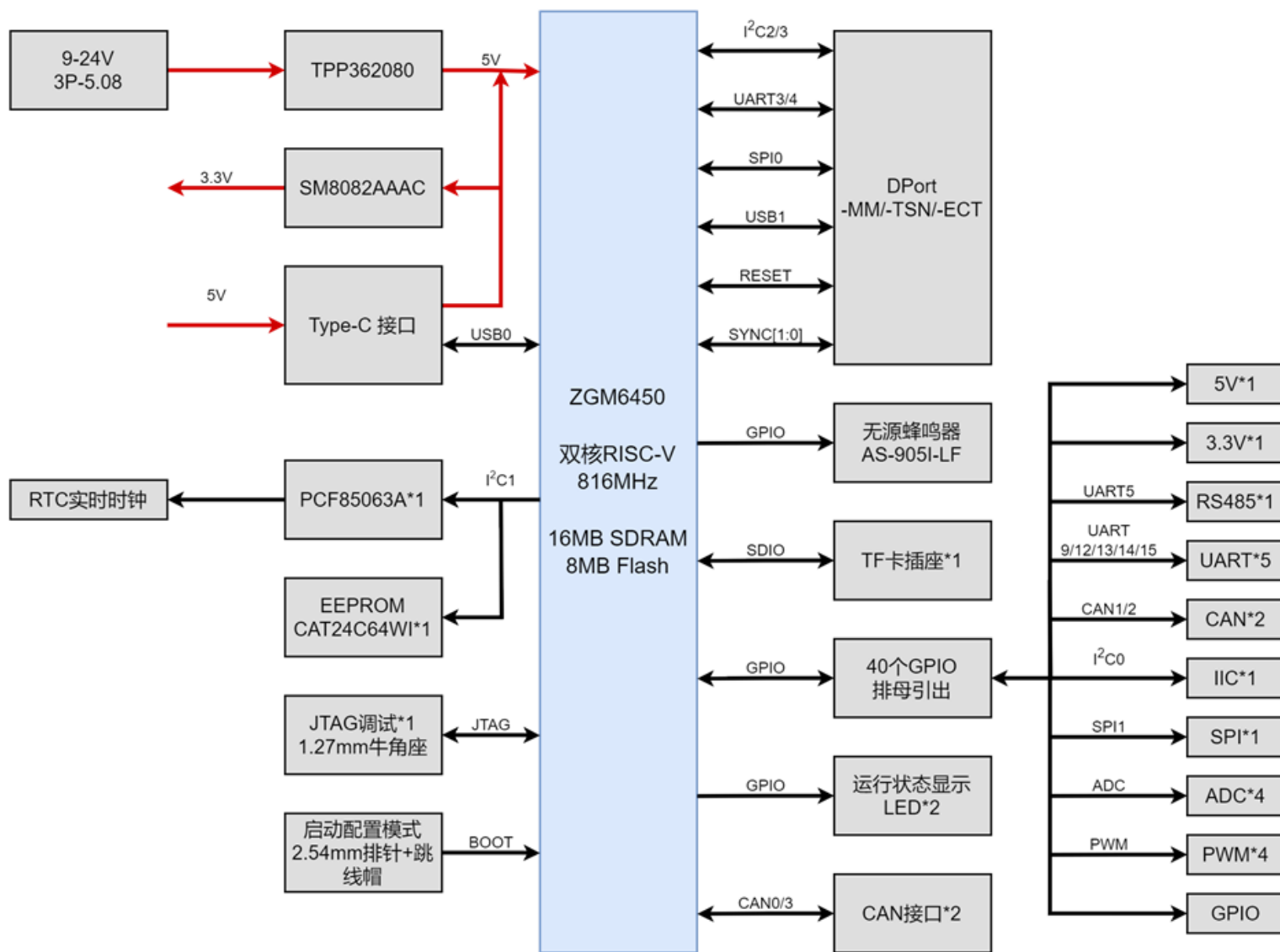


EPC6450-DP系统框图

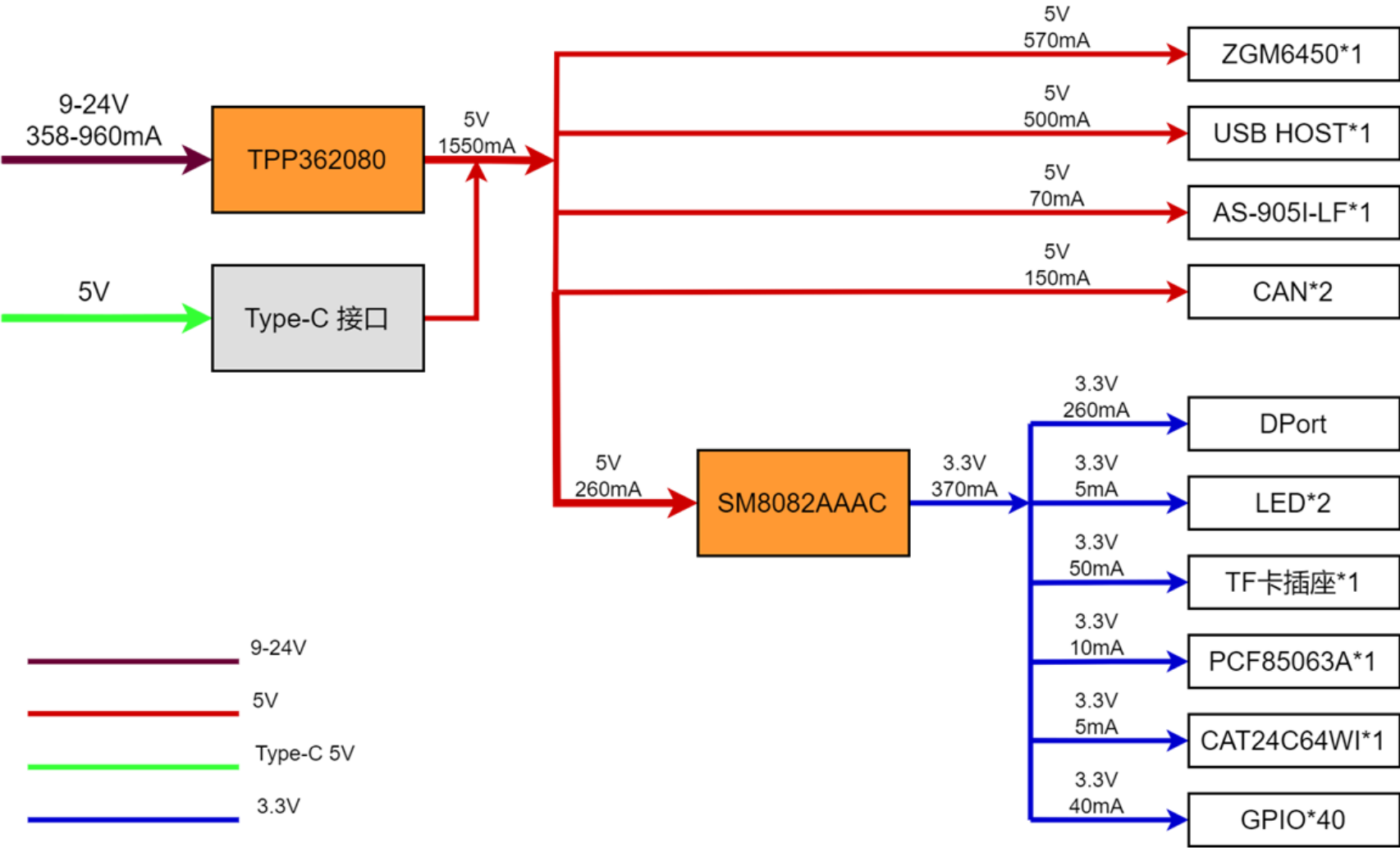


Power Rail
02. Power Rail.SchDoc

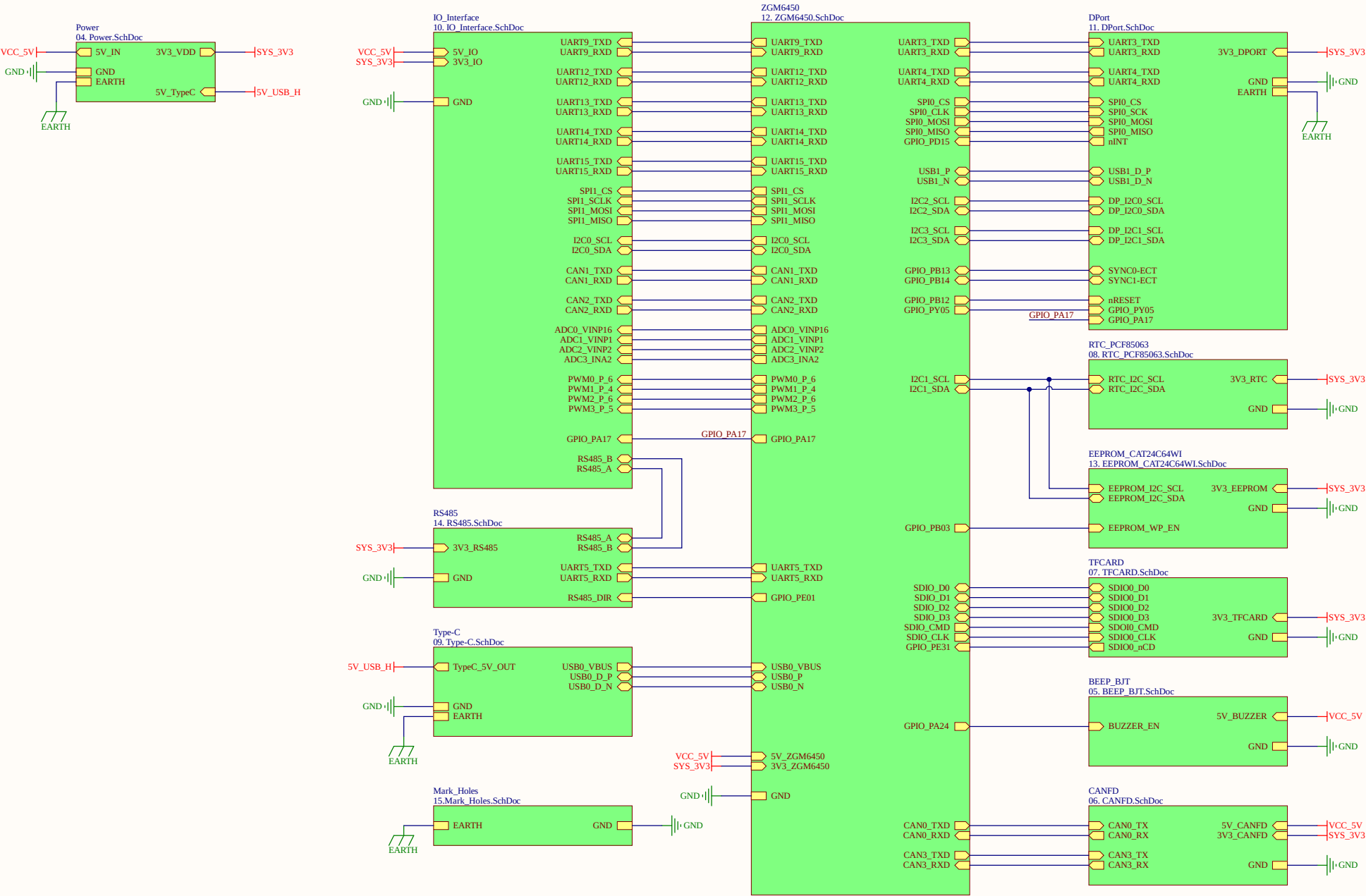
MAIN
03. MAIN.SchDoc

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2. Power Rail

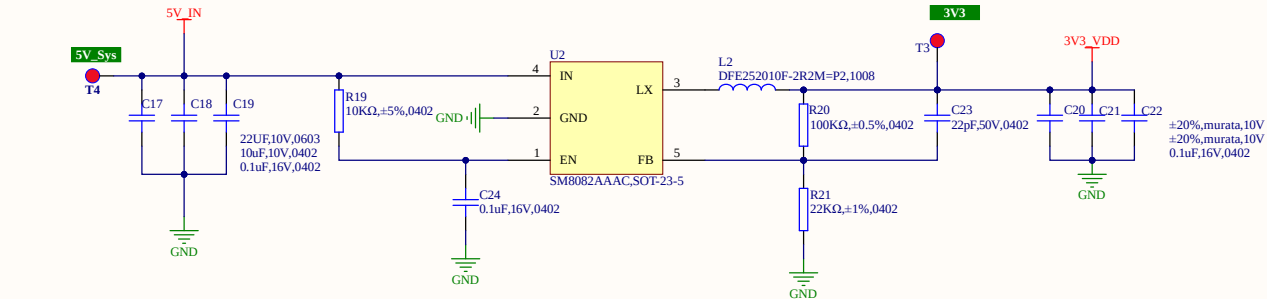
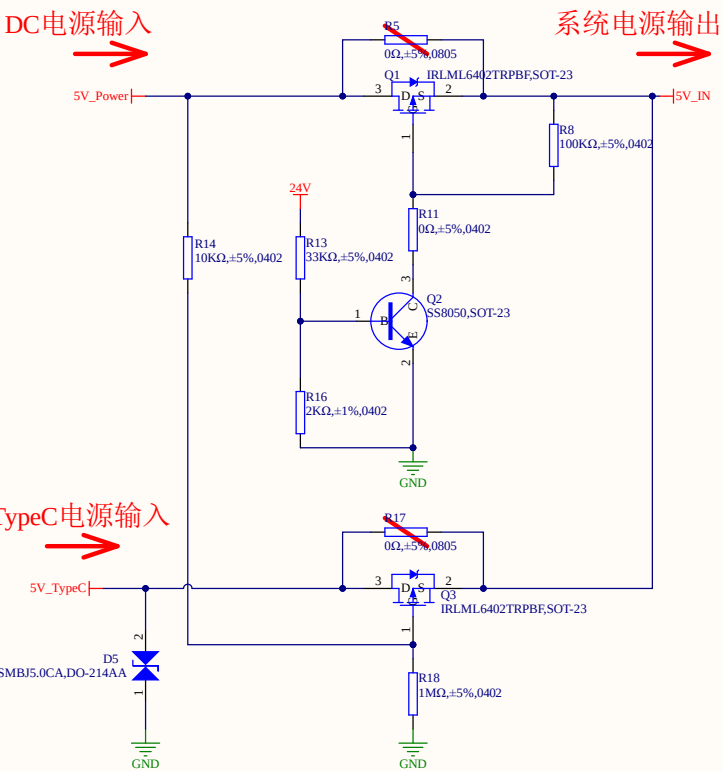
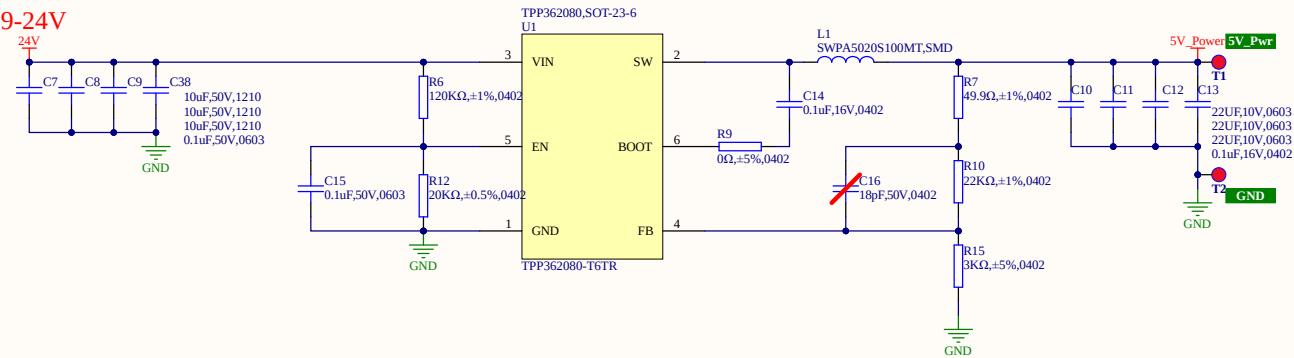
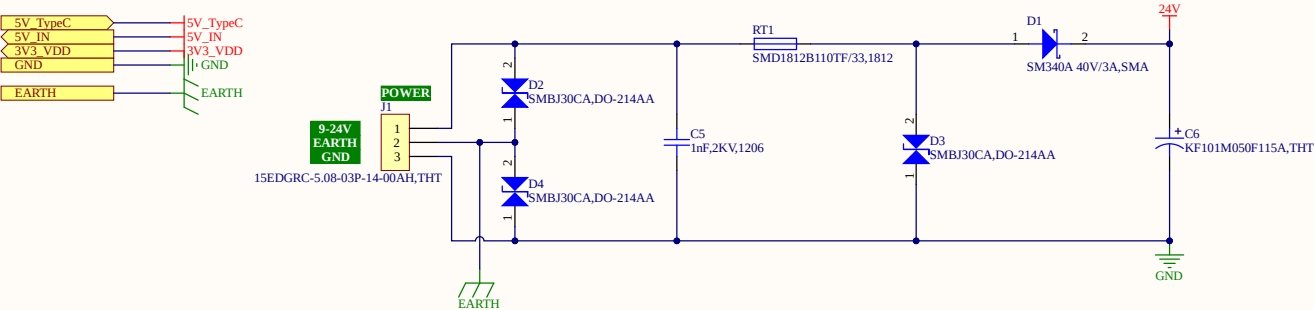


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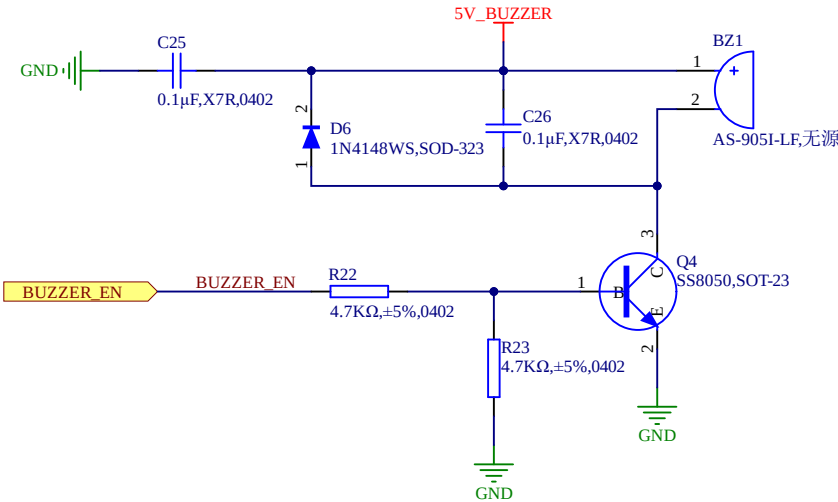
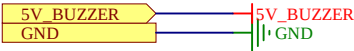
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4.Power

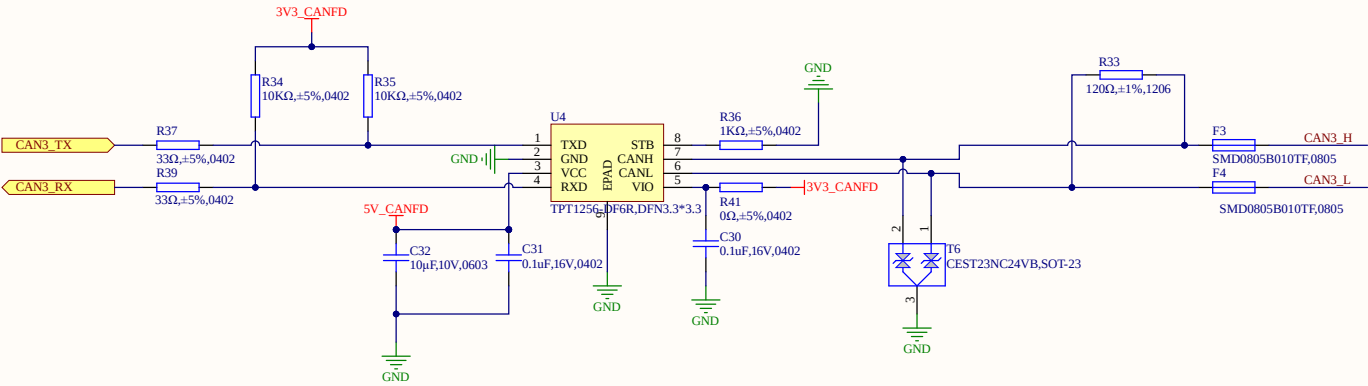
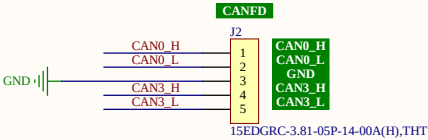
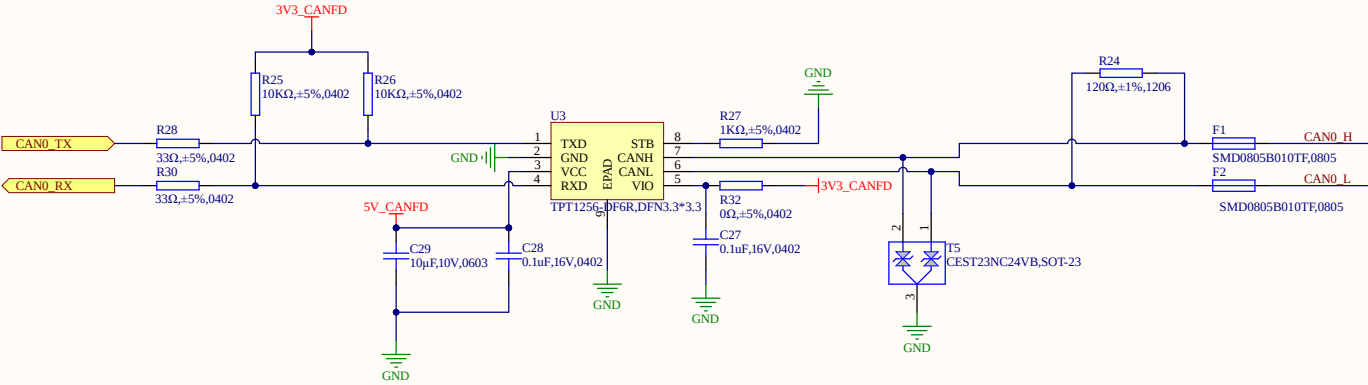
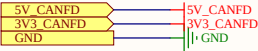


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5.BEEP_BJT

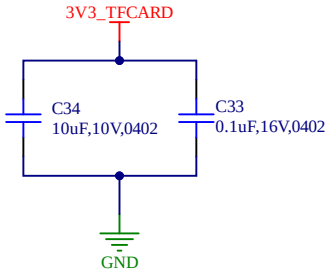
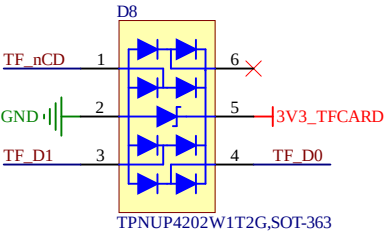
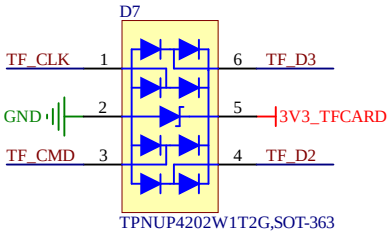
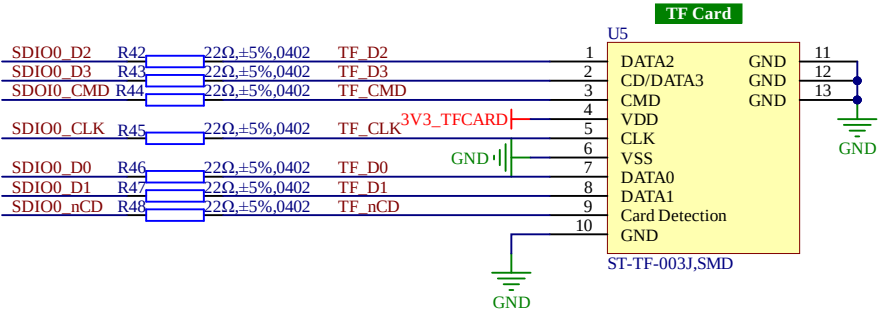
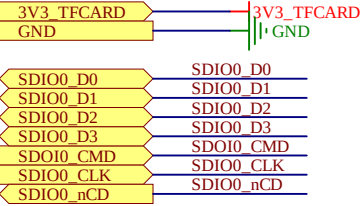


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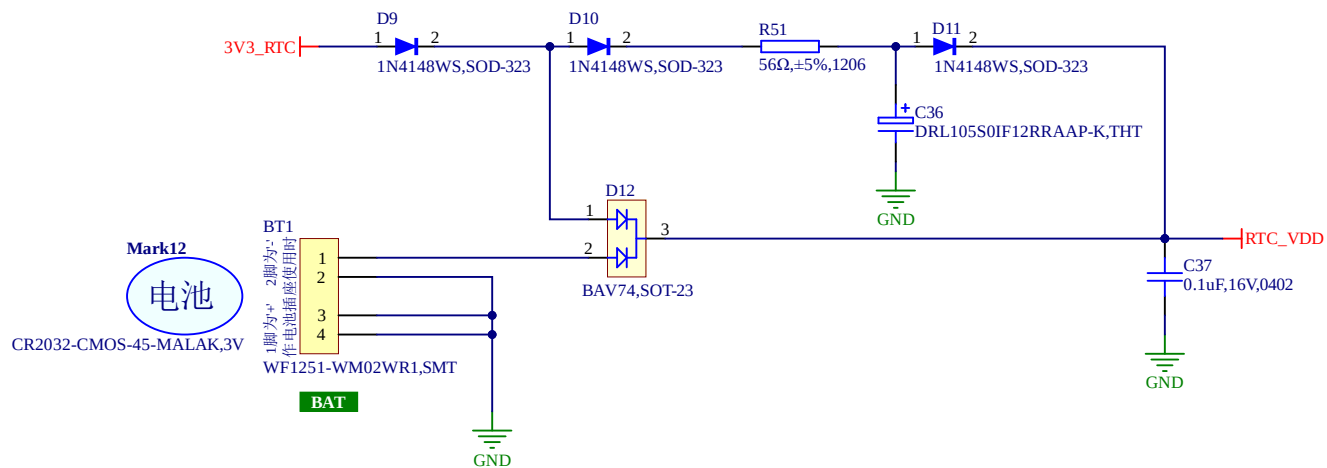
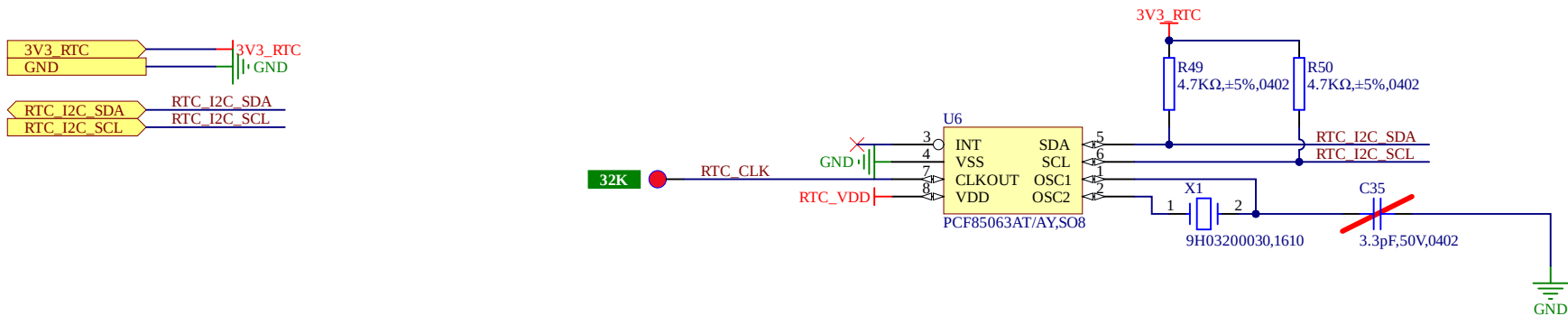
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7.TFCARD



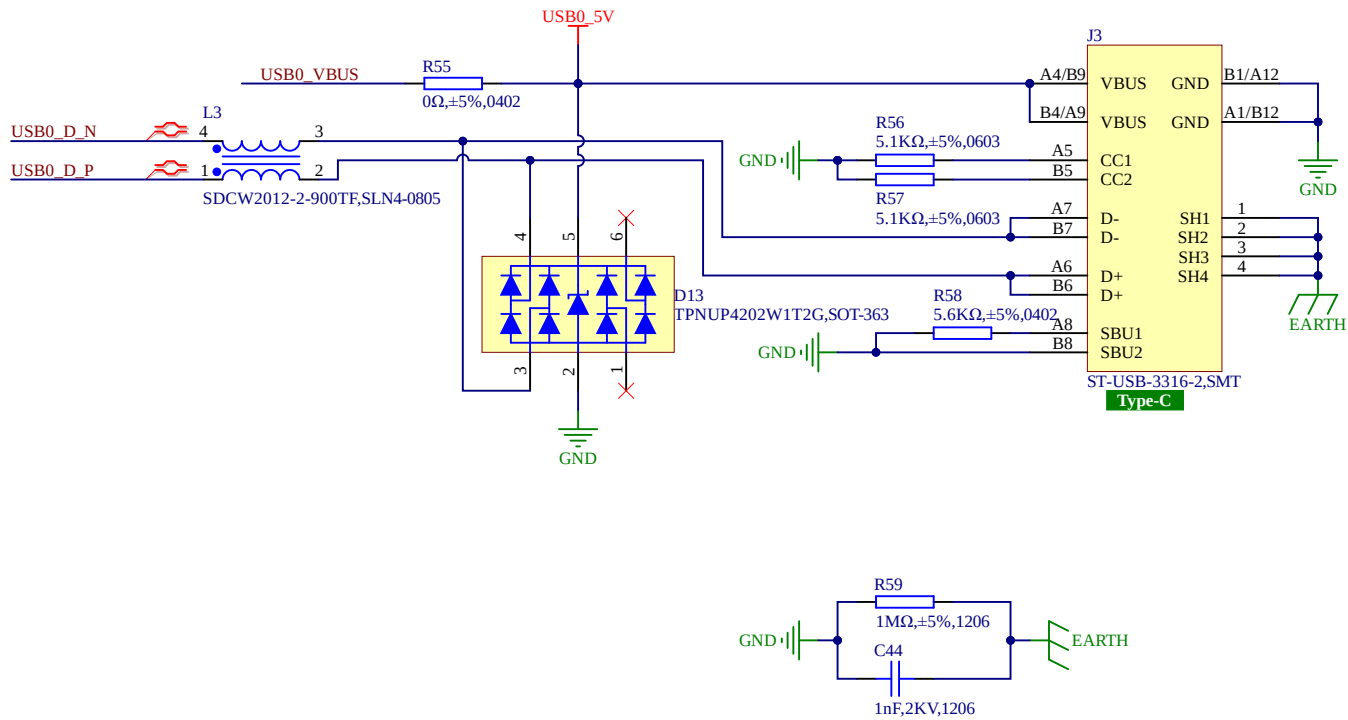
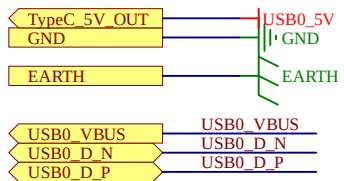
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8.RTC_PCF85063



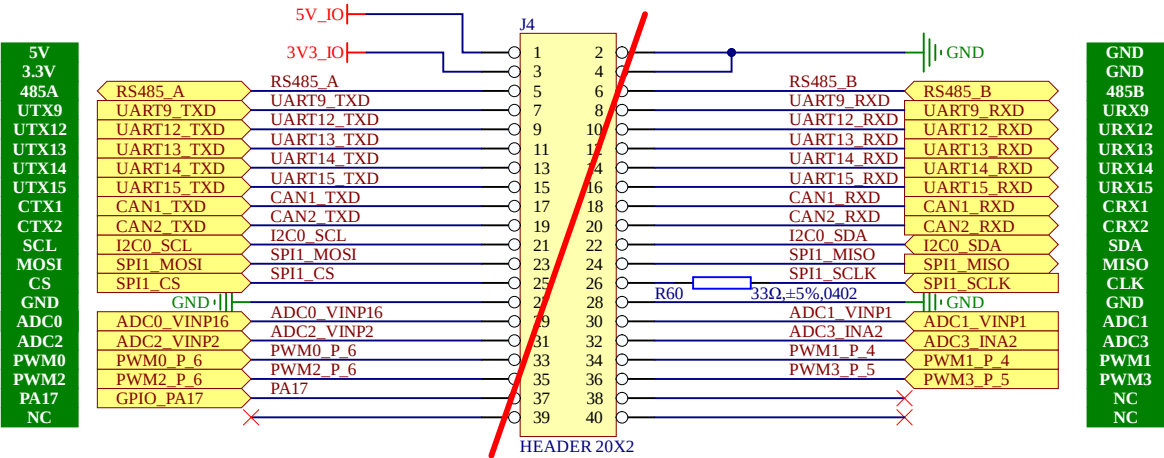
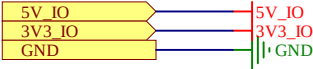
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9.Type-C



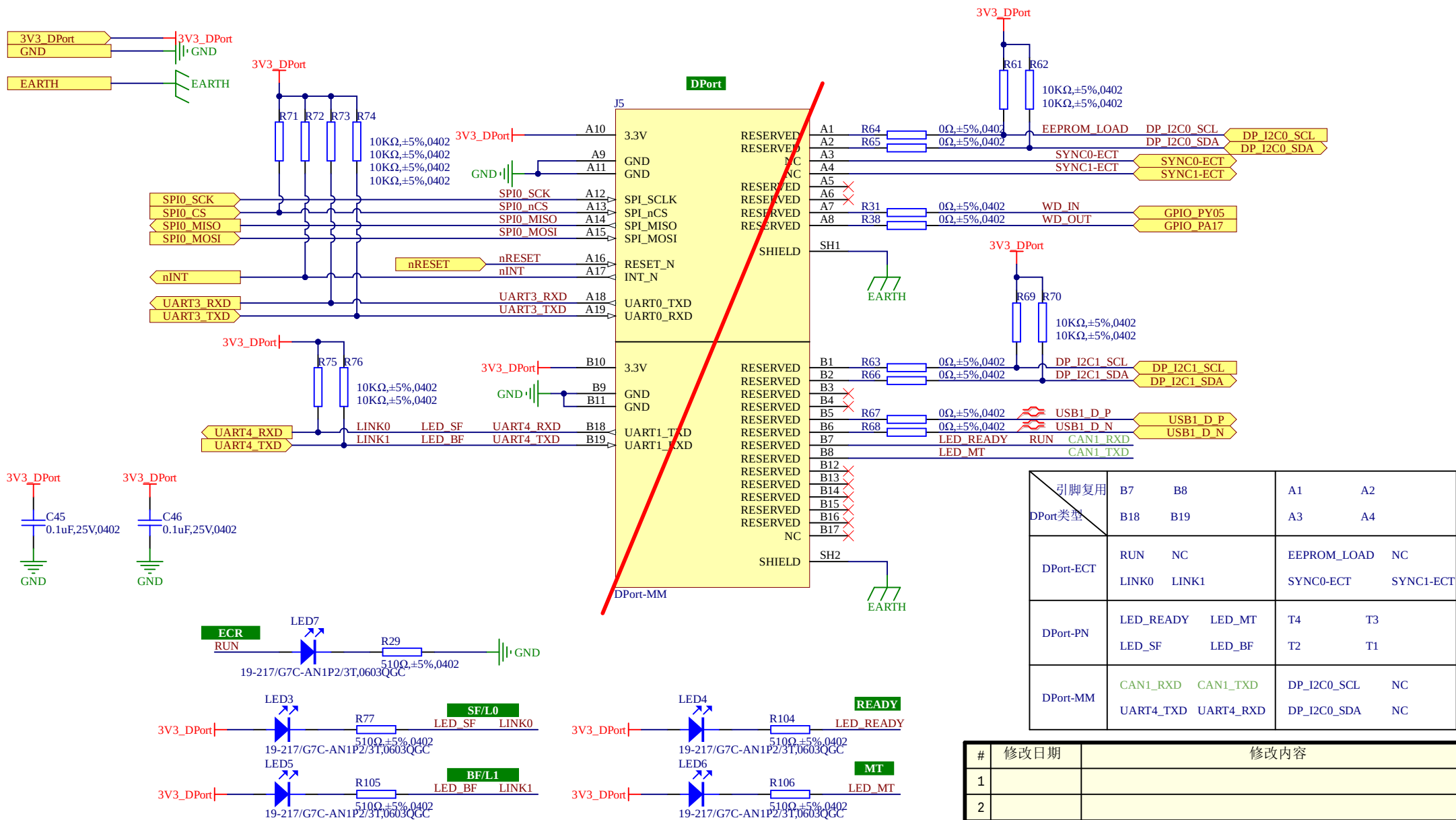
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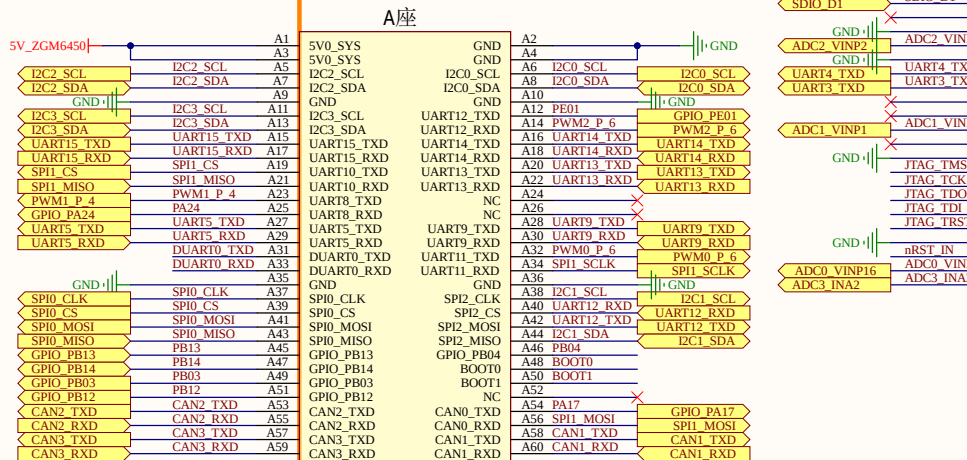
10.IO_Interface



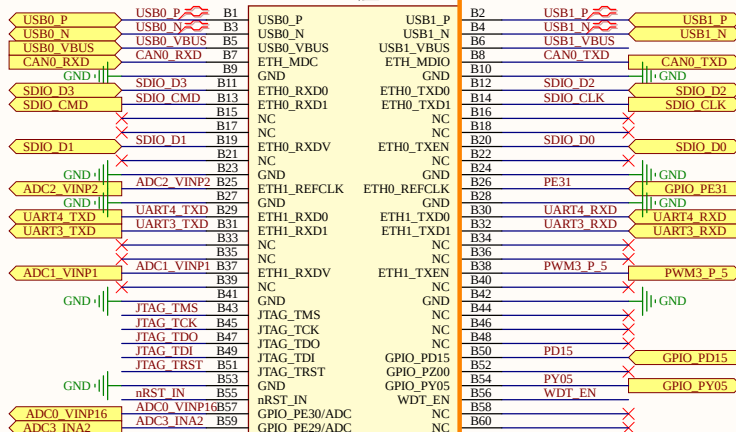
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11.DPort





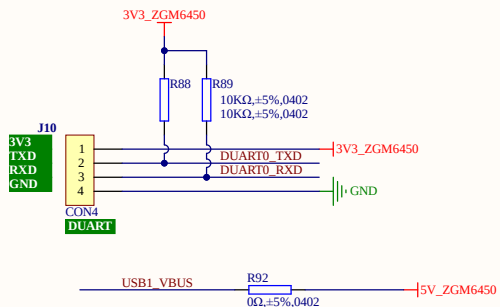
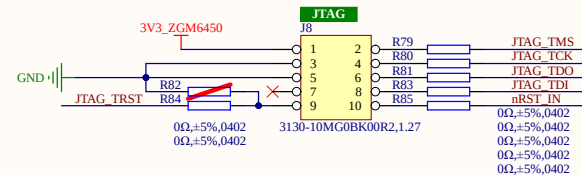
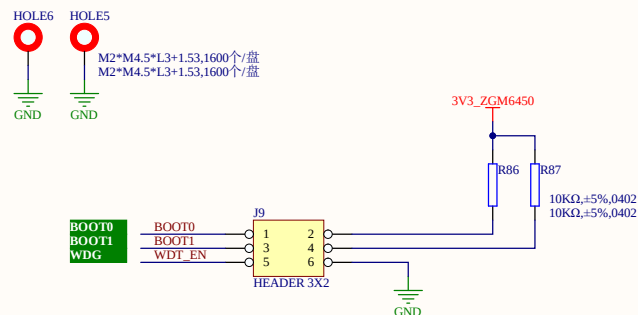
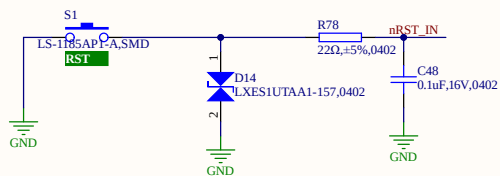
ZGM6450系列核心板



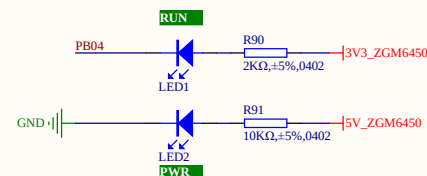
DF12NA(3.0)-60

T DF12NA(3.0)-60DP-0.5V(51),SMT

ZGM6450 S0.01

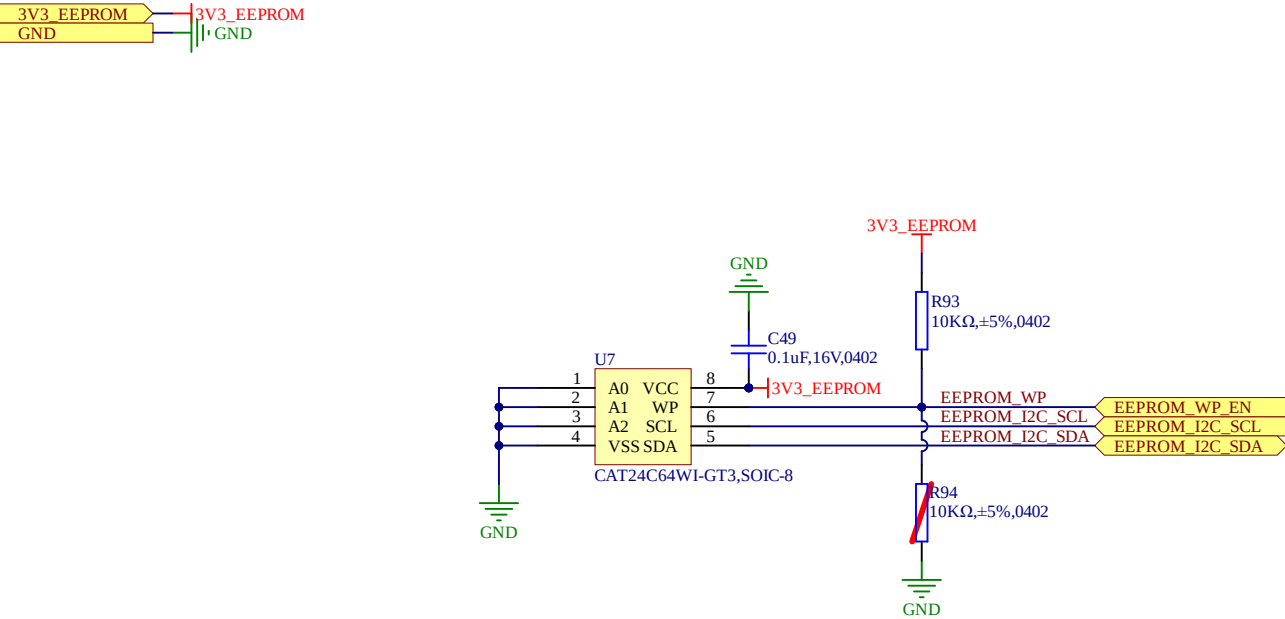


启动模式选择引脚		启动模式	说明
BOOT_MODE1	BOOT_MODE0		
0	0	XPI NOR 启动	从连接到 XPI0/1 上的串行 NOR FLASH 启动
0	1	串行启动 UART0/USB-HID	从 UART0/USB0 上启动
1	0	在系统编程 (ISP)	从 UART0/USB0 上烧写固件。
1	1	保留模式	OTP 保留模式



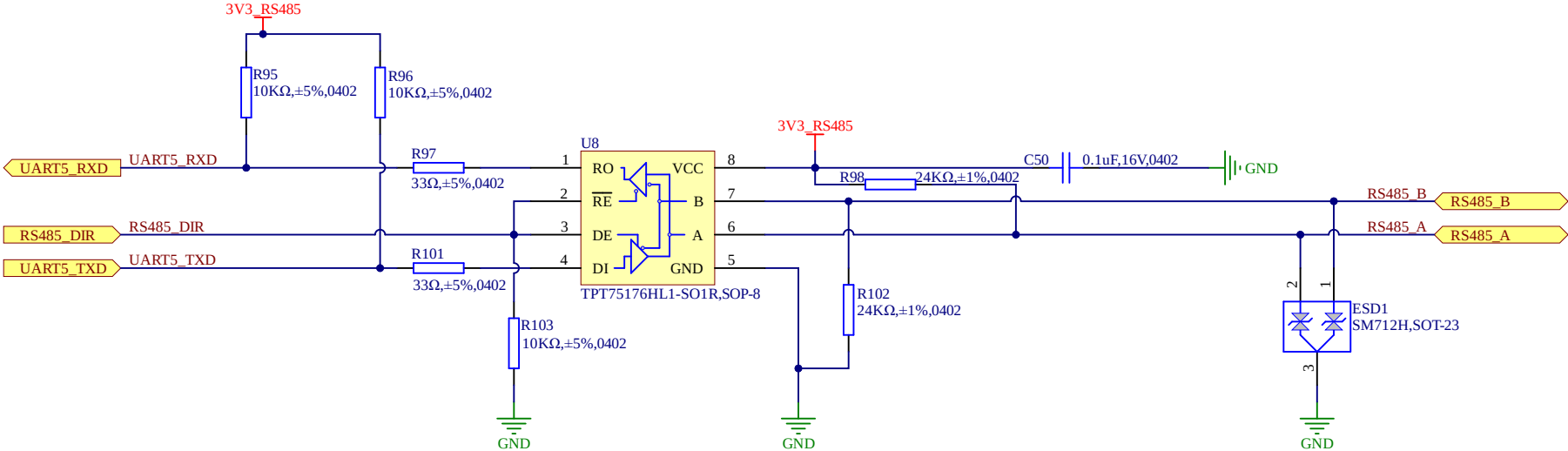
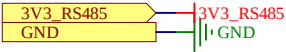
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13.EEPROM_CAT24C64WI



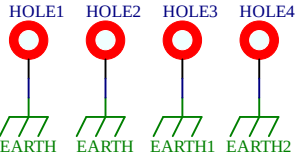
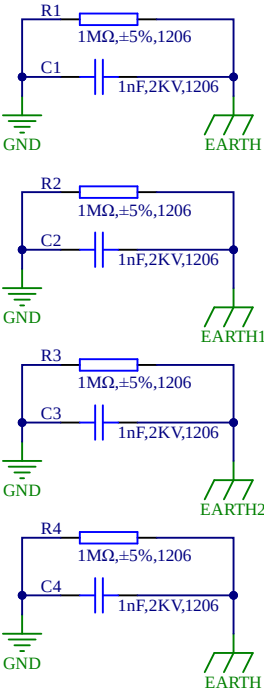
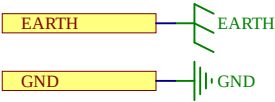
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14.RS485_TP8485E



#	修改日期	修改内容
1		
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15.Mark_Holes



#	修改日期	修改内容
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